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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Ishai Nachumovsky & Zichron Yaakov

Assignee: Tower Semiconductor, Ltd.

Title: Efficient Test Structure For Non-Volatile Memory
And Other Semiconductor Integrated Circuits

Serial No.: 09/975,064

Group No.: 2133

Filed: 10/10/2001

Examiner: Christine Tu

Docket No.: TSL-040

June 15, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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JUN 25 2004
CANCELLED
Technology Center 2600

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Sir:

Applicant submits the following amendment and remarks
in response to the Office Action dated January 28, 2004. A
Request for Extension of Time, extending the period for
response to June 28, 2004, accompanies the present response.

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